

WHAT IS CLAIMED IS:

1. A receiver circuit comprising:

5 an error detection circuit configured to receive a digital signal comprising in-phase (I) and quadrature (Q) components and to detect an error in a complex image correction factor; and

an error correction circuit coupled to said error detection circuit and configured to
10 modify said complex image correction factor dependent upon said error.

2. The receiver circuit as recited in claim 1, wherein said error detection circuit and said error correction circuit are coupled in a feedback loop, wherein said error detection circuit is configured to iteratively detect said error in said complex image correction
15 factor, and wherein said error correction circuit is configured to iteratively correct said error.

3. The receiver circuit as recited in claim 2, wherein said error detection circuit is configured to iteratively detect said error and said error correction circuit is configured to
20 iteratively correct said error until a threshold time has elapsed.

4. The receiver circuit as recited in claim 2, wherein:

said error detection circuit is further configured to combine said digital signal
25 with said complex image correction factor, to square a result of said combination, and to select a complex error component corresponding to said error in said complex image correction factor from said squared result; and

said error correction circuit is further configured to scale said complex error component by a scaling factor and to accumulate said scaled error component into said complex image correction factor.

- 5 5. The receiver circuit as recited in claim 4, wherein combining said digital signal with said complex image correction factor further comprises:

 multiplying said in-phase component by a value of a first function of the real
 portion of said complex image correction factor to form a first product;
10 multiplying said in-phase component by a value of a first function of the
 imaginary portion of said complex image correction factor to form a
 second product;
 multiplying said quadrature component by a value of a second function of the real
 portion of said complex image correction factor to form a third product;
15 multiplying said quadrature component by a value of a second function of the
 imaginary portion of said complex image correction factor to form a fourth
 product;
 accumulating said first and fourth products; and
 accumulating said second and third products.

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6. The receiver circuit as recited in claim 4, wherein said scaling factor is programmable.

7. The receiver circuit as recited in claim 6, wherein said scaling factor is
25 programmed with different values during successive iterations.

8. The receiver circuit as recited in claim 4, wherein said digital signal is dependent upon a calibration tone, and wherein selecting an error component from said squared result comprises providing a notch filter tuned to select said error component.

5 9. The receiver circuit as recited in claim 4, wherein said digital signal is dependent upon a radio frequency signal input, wherein selecting an error component from said squared result comprises detecting whether a radio frequency signal is present, and wherein accumulating said scaled error component into said complex image correction factor occurs dependent on said detecting.

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10. A method comprising:

receiving a digital signal comprising in-phase (I) and quadrature (Q) components;

15 combining said digital signal with a complex error correction factor;

detecting an error in said complex image correction factor in response to said combining; and

20 modifying said complex image correction factor dependent upon said error.

11. The method as recited in claim 10, wherein said combining further comprises iteratively combining said digital signal with said modified complex image correction factor, wherein said detecting an error further comprises iteratively detecting an error in
25 said modified complex image correction factor, and wherein said modifying said complex image correction factor further comprises iteratively modifying said complex image correction factor dependent upon said error.

12. The method as recited in claim 11, wherein said iterative combining, iterative detecting, and iterative modifying continue until a threshold time has elapsed.

5 13. The method as recited in claim 10, wherein detecting an error further comprises squaring a result of said combining and selecting a complex error component corresponding to said error in said complex image correction factor from said squared result, and wherein modifying said complex image correction factor further comprises scaling said complex error component by a scaling factor and accumulating said scaled error component into said complex image correction factor.

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14. The method as recited in claim 13, wherein said scaling factor is programmable.

15. The method as recited in claim 14, wherein said scaling factor is programmed with different values during successive iterations.

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16. The method as recited in claim 10, wherein combining said digital signal with said complex image correction factor further comprises:

20 multiplying said in-phase component by a value of a first function of the real portion of said complex image correction factor to form a first product;
multiplying said in-phase component by a value of a first function of the imaginary portion of said complex image correction factor to form a second product;
multiplying said quadrature component by a value of a second function of the real
25 portion of said complex image correction factor to form a third product;
multiplying said quadrature component by a value of a second function of the imaginary portion of said complex image correction factor to form a fourth product;

accumulating said first and fourth products; and
accumulating said second and third products.

17. The method as recited in claim 13, wherein said digital signal is dependent upon a
5 calibration tone, and wherein selecting an error component from said squared result
comprises providing a notch filter tuned to select said error component.

18. The method as recited in claim 13, wherein said digital signal is dependent upon a
radio frequency signal input, wherein selecting an error component from said squared
10 result comprises detecting whether a radio frequency signal is present, and wherein
accumulating said scaled error component into said complex image correction factor
occurs dependent on said detecting.

15 19. A computer-accessible medium comprising program instructions, wherein the
program instructions are executable by a processor to:

receive a digital signal comprising in-phase (I) and quadrature (Q) components;
detect an error in a complex image correction factor; and
20 modify said complex image correction factor dependent upon said error.

20. The computer-accessible medium as recited in claim 19, wherein said processor is
a digital signal processor (DSP).

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